



NATIONAL INSTITUTE OF TECHNOLOGY

ROURKELA-769008 (ORISSA)

MARK SHEET

BACHELOR OF TECHNOLOGY

Name : **SUCHISMIT MAHAPATRA**

Branch : **COMPUTER SCIENCE & ENGG.**

Roll No. : **P-01616**

Joined : **AUGUST 2001** Graduated : **MAY 2005**

1st Semester						2nd Semester					
SUBJECTS	Maximum Marks		Marks Secured		TOT.	SUBJECTS	Maximum Marks		Marks Secured		TOT.
	S.E.	T.E.	S.E.	T.E.			S.E.	T.E.	S.E.	T.E.	
1. English	40	10	34	7	41	1. Industrial Psychology	40	10	28	8	36
2. Economics	40	10	27	9	36	2. Physics - II	40	10	32	8	40
3. Political Science	40	10	29	7	36	3. Chemistry - II	40	10	27	6	33
4. Physics - I	40	10	29	6	35	4. Machine Drawing / Graphics Stat.	40	10	28	9	37
5. Chemistry - I	40	10	27	6	33	5. Engineering Mechanics	80	20	63	12	75
6. Engg. Drawing/ Geom. Drawing	40	10	25	7	32	6. Mathematics - II	80	20	61	16	77
7. Mathematics - I	80	20	60	13	73	7. Basic Electronics / Build. Mate. Sur.	80	20	60	12	72
8. Basic Electrical Engg.	80	20	70	16	86						
THEORY TOTAL	500		301	71	372	THEORY TOTAL	500		299	71	370
SESSIONAL WORKS :						SESSIONAL WORKS :					
1. Workshop	50				37	1. Workshop - II / Building Drawing	50				41
2. Geometrical Drawing	50				41	2. Machine Drawing / Graphics Stat.	50				40
3. Physics Lab. - I	50				40	3. Physics Lab. - II / Sur.Field	50				40
4. Chemistry Lab. - I	50				40	4. Chemistry Lab. - II	50				41
SESSIONAL TOTAL	200				158	SESSIONAL TOTAL	200				162
SEMESTER TOTAL	700				530	SEMESTER TOTAL	700				532

3rd Semester						4th Semester					
SUBJECTS	Maximum Marks		Marks Secured		TOT.	SUBJECTS	Maximum Marks		Marks Secured		TOT.
	S.E.	T.E.	S.E.	T.E.			S.E.	T.E.	S.E.	T.E.	
1. Mathematics - III	80	20	66	19	85	1. Analog Electronics - II	80	20	64	18	82
2. Analog Electronics - I	80	20	65	17	82	2. Data Structures	80	20	64	19	83
3. Discrete Mathematical Structure	80	20	70	18	88	3. Digital Electronics	80	20	48	14	62
4. Circuit Theory and Machines	80	20	58	19	77	4. Computer Organisation	80	20	59	15	74
5. Computer Programming	80	20	60	18	78	5. Mathematics - IV	80	20	74	17	91
THEORY TOTAL	500		319	91	410	THEORY TOTAL	500		309	83	392
SESSIONAL WORKS :						SESSIONAL WORKS :					
1. Programming Lab. - I	100				76	1. Computer Organisation Lab.	50				41
2. Electrical Engg. Lab.	50				46	2. Digital Electronics Lab.	100				93
3. Electronics Lab.	50				47	3. Programming Lab - II	50				42
SESSIONAL TOTAL	200				169	SESSIONAL TOTAL	200				176
SEMESTER TOTAL	700				579	SEMESTER TOTAL	700				568

REMARKS : Pass marks 35% of the full marks subject to a minimum of 30% in each written paper of semester exam. and 50% of the full marks in the sessionals.

(Continued overleaf)

Name : **SUCHISMIT MAHAPATRA**Branch : **COMPUTER SCIENCE & ENGG.**Roll No. : **P-01616**

5th Semester					
SUBJECTS	Maximum Marks		Marks Secured		
	S.E.	T.E.	S.E.	T.E.	TOT.
1. Switching and Automata Theory	60	40	42	31	73
2. System Programming	60	40	43	29	72
3. Database Management System	60	40	41	34	75
4. Microprocessor - I	60	40	41	36	77
5. Control System Engg.	60	40	38	35	73
THEORY TOTAL	500		205	165	370
SESSIONAL WORKS :					
1. Programming Lab. - III	100				86
2. Microprocessor Lab. - I	50				39
3. Integrated Circuit Lab.	50				45
SESSIONAL TOTAL	200				170
SEMESTER TOTAL	700				540

6th Semester					
SUBJECTS	Maximum Marks		Marks Secured		
	S.E.	T.E.	S.E.	T.E.	TOT.
1. Computer Graphics	60	40	48	34	82
2. Algorithm Analysis	60	40	53	32	85
3. Operating System	60	40	42	29	71
4. Principles of Programming Lang.	60	40	35	24	59
5. Microprocessor - II	60	40	51	31	82
THEORY TOTAL	500		229	150	379
SESSIONAL WORKS :					
1. Computer Graphics Lab.	50				37
2. Microprocessor Lab. -II	50				36
3. Programming Lab. - IV	100				66
SESSIONAL TOTAL	200				139
SEMESTER TOTAL	700				518

7th Semester					
SUBJECTS	Maximum Marks		Marks Secured		
	S.E.	T.E.	S.E.	T.E.	TOT.
1. Advance Control System	60	40	56	37	93
2. Data Comm. and Comp. Network	60	40	43	29	72
3. Information System Design	60	40	44	28	72
4. Compiler Construction	60	40	57	35	92
5. Operation Research	60	40	48	24	72
THEORY TOTAL	500		248	153	401
SESSIONAL WORKS :					
1. Project (Minor)	50				41
2. Data Communication Lab.	50				44
3. Microprocessor Lab. - III	50				44
4. System Programming - III	50				38
SESSIONAL TOTAL	200				167
SEMESTER TOTAL	700				568

8th Semester					
SUBJECTS	Maximum Marks		Marks Secured		
	S.E.	T.E.	S.E.	T.E.	TOT.
1. Artificial Intelligence	60	40	30	25	55
2. Digital Signal Processing	60	40	50	34	84
3. Software Engineering	60	40	29	28	57
4. Advance Computer Architecture	60	40	32	31	63
THEORY TOTAL	400		141	118	259
SESSIONAL WORKS :					
1. Project (Major)	100				78
2. Seminar	50				40
3. Viva-voce	50				42
4. Practical Training	50				42
5. Artificial Intelligence Lab.	50				35
SESSIONAL TOTAL	300				237
SEMESTER TOTAL	700				496

50% of First Semester	350	265.0
50% of Second Semester	350	266.0
50% of Third Semester	350	289.5
50% of Fourth Semester	350	284.0
100% of Fifth Semester	700	540
100% of Sixth Semester	700	518
100% of Seventh Semester	700	568
100% of Eighth Semester	700	496
GRAND TOTAL	4200	3227

- REMARKS: 1. 1st Class with Hons.: 75% or above the maximum marks; 3150 or above
 2. 1st Class: 60% or above but below 75% ; 2520 or above but below 3150
 3. 2nd Class: 50% or above but below 60% ; 2100 or above but below 2520
 4. Pass : All others below 2100

Date : 31-May-05

Verified By :

[Signature]
31/05/05

[Signature]
31.5.2005
CONTROLLER EXAMINATIONS/ DEAN (ACAD.)